



1fcw

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

Applicant: Edward J. Nowak et al
Serial No.: 10/709076
Filed: 04/12/2004
Title: FinFET Transistor and Circuit.

Art Unit:
Examiner:
Atty. Docket: BUR920040022US1

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR 1.56, 1.97, 1.98

Honorable Commissioner of Patents and Trademarks
Washington, D. C. 20231

Sir:

Applicants submit herewith form PTO-1449, listing patents, publications, or other information of which they are aware which they believe may be material to patentability pursuant to 37 CFR 1.56(b), and in respect of which there may be a duty to disclose under 37 CFR 1.56(a), together with legible copies of the patents, publications, or other information listed.

While the items submitted with this Information Disclosure Statement may be material to patentability pursuant to 37 CFR 1.56, in accordance with 37 CFR 1.97(h) it shall not be construed to be an admission that any patent, publication, or other information cited is "prior art" or is material to patentability for this invention unless specifically designated as such. In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other information material to patentability, as defined in 37 CFR 1.56(b), exists.

Date: Jul. 8, 2004

By:

Respectfully submitted,



William D. Sabo, Esq.
Registration No. 27,465
Attorney for Applicants
IBM Corporation
Intellectual Property Law - Mail Stop 972E
1000 River Street
Essex Junction, Vermont 05452

INFORMATION DISCLOSURE CITATION <small>(Use several sheets if necessary)</small>				Docket Number (Optional) BUR920040022US1		Application Number 10/709076		
				Applicant(s) Edward Nowak et al.				
				Filing Date 04/12/2004		Group Art Unit Group Art Unit		
U.S. PATENT DOCUMENTS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
U.S. PATENT APPLICATION PUBLICATIONS								
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>								
		NEAR-CUSTOM BOOK IMAGE, IBM Technical Disclosure Bulletin, October 1992, pages 521-524.						
EXAMINER				DATE CONSIDERED				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								